L Number	Hits	Search Text	DB	Time stamp
-	0	emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and (maintenance adj bus$2) and controller$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 15:18
-	1	(multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and (maintenance adj bus$2) and controller$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/02 16:51
-	76	(BEAUSOLEIL-WILLIAM-F BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG TANNENBAUM-PETER THOMAS-LAWRENCE THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A" TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES TOMASSETTI-ET-AL).in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 10:44
-	12	((BEAUSOLEIL-WILLIAM-F BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG TANNENBAUM-PETER THOMAS-LAWRENCE THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A" TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES TOMASSETTI-ET-AL).in.) and emulat$6	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/02 16:58
-	51	((BEAUSOLEIL-WILLIAM-F BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG TANNENBAUM-PETER THOMAS-LAWRENCE THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A" TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES TOMASSETTI-ET-AL).in.) and (emulat$6 or $4RAM or bus$2)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/02 16:59
-	6	((BEAUSOLEIL-WILLIAM-F BEAUSOLEIL-WILLIAM-FRANCIS COOK-RICHARD-B COOK-ROBERT-B COOK-ROBERT-BRADLEY COOK-ROGER-B COOK-ROY-B NG-TAK-KWONG TANNENBAUM-PETER THOMAS-LAWRENCE THOMAS-LAWRENCE-A "THOMAS-LAWRENCE-S.A" TOMASSETTI-NORTON-J TOMASSETTI-N-JAMES TOMASSETTI-ET-AL).in.) and (emulat$6 and $4RAM and bus$2)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 10:44
-	36	emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and (bus$2) and controller$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/02 19:35
-	35	(ROTH-HELMUT).in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 10:44
-	2	((ROTH-HELMUT).in.) and (emulat$6 and $4RAM and bus$2)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 14:35
-	1	5551013.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 14:36
-	1	6618698.pn. and bus$2	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 14:39
-	1	6618698.pn. and $4RAM	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 14:39
-	36	emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and (bus$2) and controller$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 16:24

-		9	(emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and (bus$2) and controller$1) and @ad<20000906	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 16:24
-		36	emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and ((mov$3 or cop$4 or transfer$4) same data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 15:40
-		9	(emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and ((mov$3 or cop$4 or transfer$4) same data)) and @ad<20000906	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 15:56
-		8	((emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and ((mov$3 or cop$4 or transfer$4) same data)) and @ad<20000906) and (halt$3 or stop$4)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 15:57
-		4	(((emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and ((mov$3 or cop$4 or transfer$4) same data)) and @ad<20000906) and (halt$3 or stop$4)) and (resum$3 or restart$4)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 15:57
-		27	emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and refresh$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 16:24
-		9	(emulator$1 and (multi$processor$1 or multi$cpu) and (processor$1 or cpu$1) and SDRAM$1 and refresh$3) and @ad<20000906	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/12/03 16:24